

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated April 19, 2005 has been received and its contents carefully reviewed.

Claim 27 is hereby amended. Claims 27-34 are pending. Reexamination and reconsideration of the pending claims are respectfully requested.

In the Office Action, claims 27-29 and 33-34 are rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,147,667 to Yamazaki et al. (hereinafter "Yamazaki"). Claims 30-32 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Yamazaki.

The rejection of claims 27-29 and 33-34 as being anticipated by Yamazaki is respectfully traversed and reconsideration is requested.

Claim 27 is allowable over Yamazaki in that the structure of claim 27 recites a combination of elements including, for example, "a first glass substrate including at least first, second and third regions; a pixel array including a switching device and a pixel electrode in the first region of the first substrate; a driver circuit in the second region of the first substrate; a controller unit in the third region of the first substrate, wherein the pixel array, driver circuit and controller unit are integrally located on the first substrate." Yamazaki does not teach at least these features of the claimed invention. Specifically, the structure of claim 27 of the present application is different from the structure of Yamazaki in that the substrate is a "glass substrate including at least first, second and third regions." On the contrary, Yamazaki merely teaches that the substrate is made of quartz. See column 14, lines 1-2 and 43-44; Figures 6A and 6B. Yamazaki teaches that since a heat treatment at a temperature of 700 to 1100 degrees Celsius is necessary, the substrate must have such heat resistance as is able to withstand the heat in the temperature range. Yamazaki further teaches that an amorphous silicon film is grown and crystallized at a high temperature. See column 16, lines 44-48; column 6, lines 14-19 and 29-31; column 7, lines 7-12. Therefore, Yamazaki teaches away from using a glass substrate because Yamazaki teaches a substrate with a high heat resistance, that is, a quartz substrate. In contrast, the glass substrate of the present application enables amorphous silicon to be crystallized at a

lower temperature. Thus, Yamazaki does not teach “a first glass substrate including at least first, second and third regions; a pixel array including a switching device and a pixel electrode in the first region of the first substrate; a driver circuit in the second region of the first substrate; a controller unit in the third region of the first substrate, wherein the pixel array, driver circuit and controller unit are integrally located on the first substrate.” Accordingly, because Yamazaki fails to teach these features of claim 27, Applicant respectfully submits that claim 27 and claims 28, 29, 33 and 34, which depend therefrom, are allowable over Yamazaki.

The rejection of claims 30-32 as being unpatentable over Yamazaki is respectfully traversed and reconsideration is requested.

Yamazaki does not teach or suggest “a first glass substrate including at least first, second and third regions; a pixel array including a switching device and a pixel electrode in the first region of the first substrate; a driver circuit in the second region of the first substrate; a controller unit in the third region of the first substrate, wherein the pixel array, driver circuit and controller unit are integrally located on the first substrate,” as recited in independent claim 27. As stated above, Yamazaki does not teach “a first glass substrate” and only teaches and suggests a quartz substrate. Therefore, Yamazaki does not contain any teachings or suggestions to cure the deficiencies as discussed above with regard to independent claim 27. For at least this reason, claims 30-32, which depend from claim 27, are allowable over Yamazaki.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

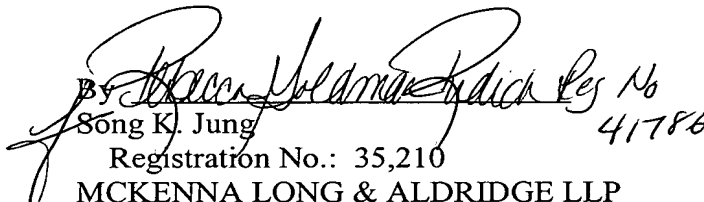
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Reply to Office Action dated April 19, 2005

Docket No.: 8733.133.00-US

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: September 28, 2005

Respectfully submitted,


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